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SC#1085

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10052063	FILING DATE 01/16/2002	CLASS 716	SUBCLASS 738	GAU 2182	EXAMINER <i>[Signature]</i>
**APPLICANTS: Cheah Jonathon; Quek Chee; Kwek Ee; 714 9167 2133					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB		DO NOT PUBLISH <input checked="" type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed		<input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no		004939P025	
Verified and Acknowledged Examiners's initials					
TITLE : In-chip monitoring system to monitor input/output of functional blocks					
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for 0.6
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drawn	Figs. Drawn
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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